

Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-137002	Application No. 09/626,535
<b>Information Disclosure Statement by Applicant</b> (Use several sheets if necessary)		Applicant Donald F. Hooper, et al.	
		Filing Date July 27, 2000	Group Art Unit 2155
(37 CFR §1.98(b))			

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Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
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	AC	2002/0041520	04/2002	Wolrich et al.			
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Examiner Signature <b>DAVID Y. ENG</b> <b>PRIMARY EXAMINER</b>	Date Considered <b>11/07/07</b>
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	FY	6,697,379	02/2004	Jacquet et al.			
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	GD	6,754,211	06/2004	Brown			
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	GH	6,792,488	09/2004	Wolrich et al.			
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	HC	7,006,495	02/2006	Hooper			
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	HX	WO 00/56024	09/2000	WIPO				
	HY	WO 01/16718	03/2001	WIPO				
	HZ	WO 01/16769	03/2001	WIPO				
	IA	WO 01/16770	03/2001	WIPO				
	IB	WO 01/16782	03/2001	WIPO				
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	IL	"Enterprise Hardware, Intel Expected to Unveil New Networking Chip," News.Com, August 26, 1999, < <a href="http://new.com.com/Intel+expected+to+unveil+new+networking+chip/2100-1001_3-230315.html">http://new.com.com/Intel+expected+to+unveil+new+networking+chip/2100-1001_3-230315.html</a> > (accessed on 08/23/05), pages 1-5.
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	IP	Chappell, et al., "Simultaneous Subordinate Microthreading (SSMT)", <i>IEEE</i> , pg. 186-195 (1999).
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	IR	<u>Digital Semiconductor 21140A PCI Fast Ethernet LAN Controller, Hardware Reference Manual</u> , Digital Equipment Corporation, pages i-x, 1-1 through 1-5, 2-1 through 2-12, 3-1 through 3-38, 4-31 through 5-2, 6-1 through 6-24, (March 1998).
	IS	Farrens, et al., "Strategies for Achieving Improved Processor Throughput", <i>ACM</i> , pg. 362-369 (1991).
	IT	Frazier, Howard, "Gigabit Ethernet: From 100 to 1,000 Mbps", <i>IEEE Internet Computing</i> , pages 24-31, (1999).
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